

IN THE SPECIFICATION

Please amend the paragraph beginning at page 7, line 5 and ending at page 7, line 8, as follows:

--The BAR 225 has 32-bits and uses one of the bits for storing endian information. The endian information of the first PCI card ~~[[200]]~~ 220 is stored in the fourth bit of the BAR 225 as "0". Endian information of the second PCI card 230 is stored in fourth bit of a BAR 235 as "1".--